

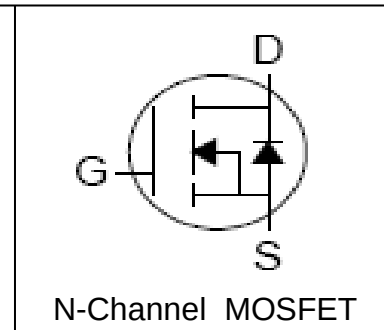
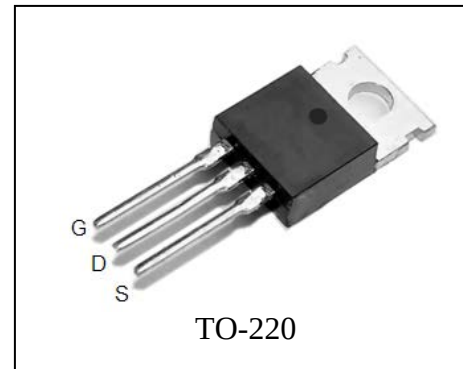
Features

- 60V/120A,
 $R_{DS(ON)} = 6m\Omega$ (Typ.) @ $V_{GS} = 10V$
- Ultra Low On-Resistance
- Exceptional dv/dt capability
- Fast Switching and Fully Avalanche Rated
- 100% avalanche tested
- 175°C Operating Temperature
- Lead Free and Green Available

Applications

- Switching Application Systems
- Inverter Systems

Pin Description



Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T _A =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		60	V
V _{GSS}	Gate-Source Voltage		±25	
T _J	Maximum Junction Temperature		175	°C
T _{STG}	Storage Temperature Range		-55 to 175	°C
I _S	Diode Continuous Forward Current	T _C =25°C	120 ^①	A
Mounted on Large Heat Sink				
I _{DP}	300μs Pulse Drain Current Tested	T _C =25°C	380 ^②	A
I _D	Continuous Drain Current	T _C =25°C	120 ^①	A
		T _C =100°C	90 ^①	
P _D	Maximum Power Dissipation	T _C =25°C	150	W
		T _C =100°C	75	
R _{θJC}	Thermal Resistance-Junction to Case		1	°C/W
Drain-Source Avalanche Ratings				
E _{AS} ^③	Avalanche Energy, Single Pulsed		625	mJ

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Condition	RU6099R			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	60			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 60V, V _{GS} =0V			1	μA
		T _J =85°C			30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2	3	4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
R _{DS(ON)} ^④	Drain-Source On-state Resistance	V _{GS} = 10V, I _{DS} =40A		6	7	mΩ
Diode Characteristics						
V _{SD} ^④	Diode Forward Voltage	I _{SD} =40A, V _{GS} =0V			1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =40A, dI _{SD} /dt=100A/μs		50		ns
Q _{rr}	Reverse Recovery Charge			95		nC
Dynamic Characteristics ^⑤						
R _G	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz		1.3		Ω
C _{iSS}	Input Capacitance	V _{GS} =0V, V _{DS} = 30V, Frequency=1.0MHz		3000		pF
C _{oSS}	Output Capacitance			430		
C _{rSS}	Reverse Transfer Capacitance			240		
t _{d(ON)}	Turn-on Delay Time	V _{DD} =30V, R _L =30Ω, I _{DS} = 1A, V _{GEN} = 10V, R _G =8Ω		14		ns
t _r	Turn-on Rise Time			17		
t _{d(OFF)}	Turn-off Delay Time			40		
t _f	Turn-off Fall Time			62		
Gate Charge Characteristics ^⑤						
Q _g	Total Gate Charge	V _{DS} =30V, V _{GS} = 10V, I _{DS} =40A		72		nC
Q _{gs}	Gate-Source Charge			13		
Q _{gd}	Gate-Drain Charge			24		

Notes: ① Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A.

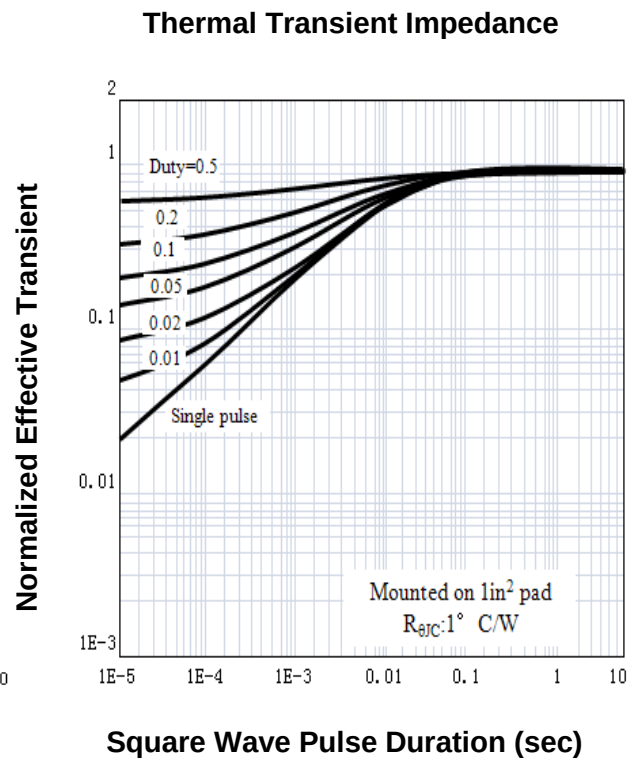
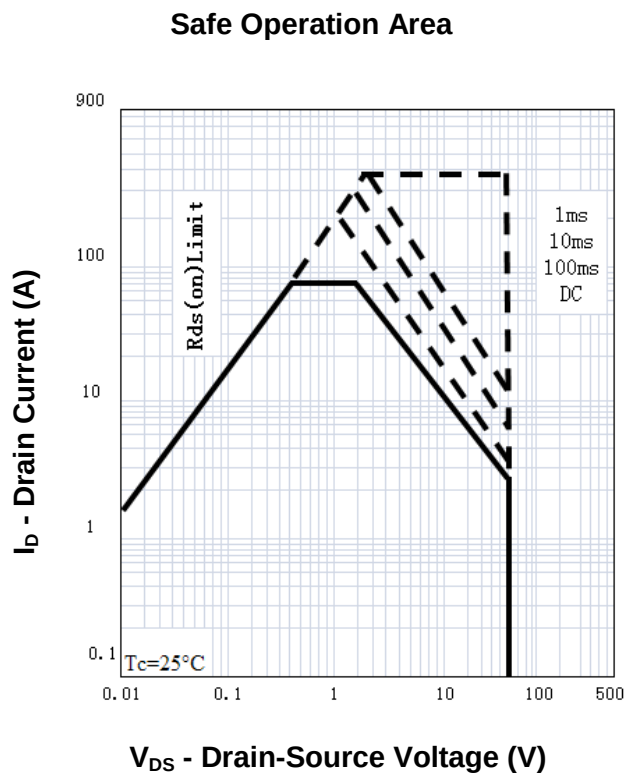
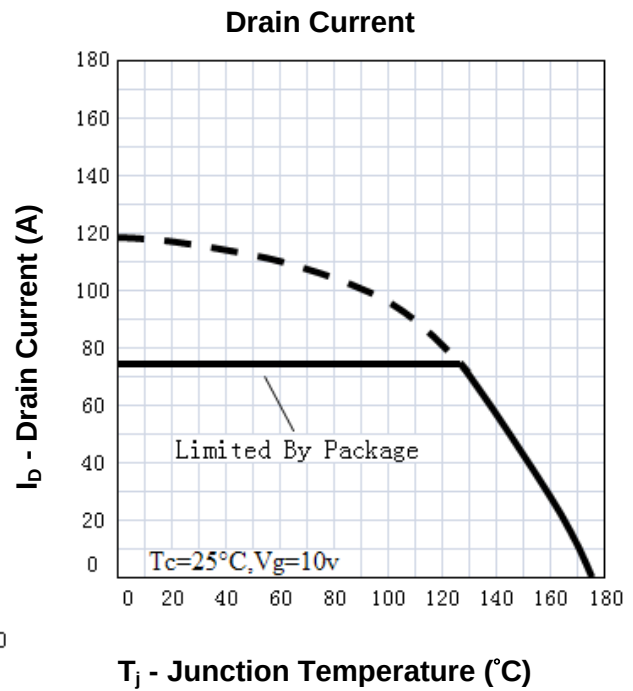
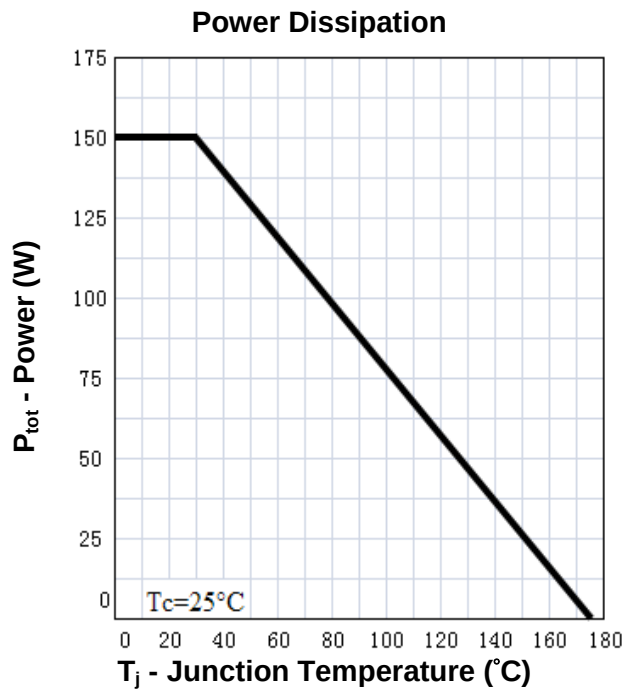
② Pulse width limited by safe operating area.

③ Limited by T_{Jmax} , $I_{AS}=27A$, $V_{DD}=48V$, $R_G=50\Omega$, Starting $T_J=25^{\circ}\text{C}$.

④ Pulse test; Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

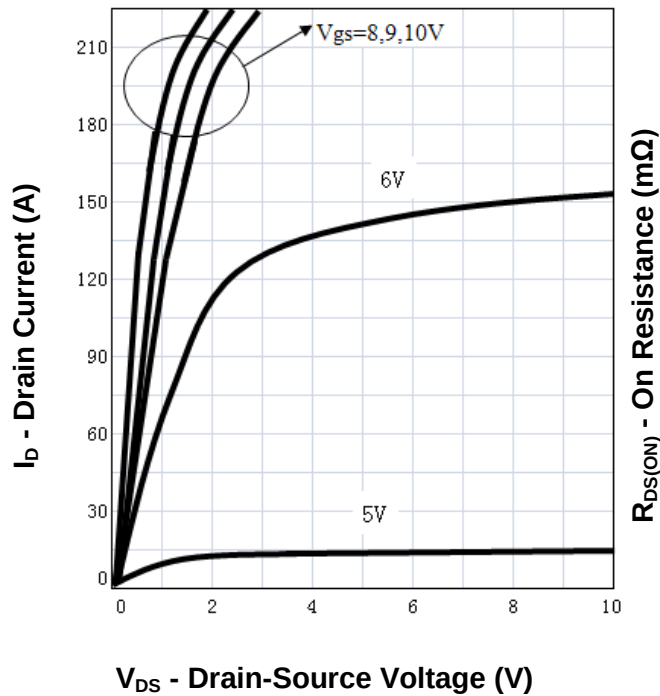
⑤ Guaranteed by design, not subject to production testing.

Typical Characteristics

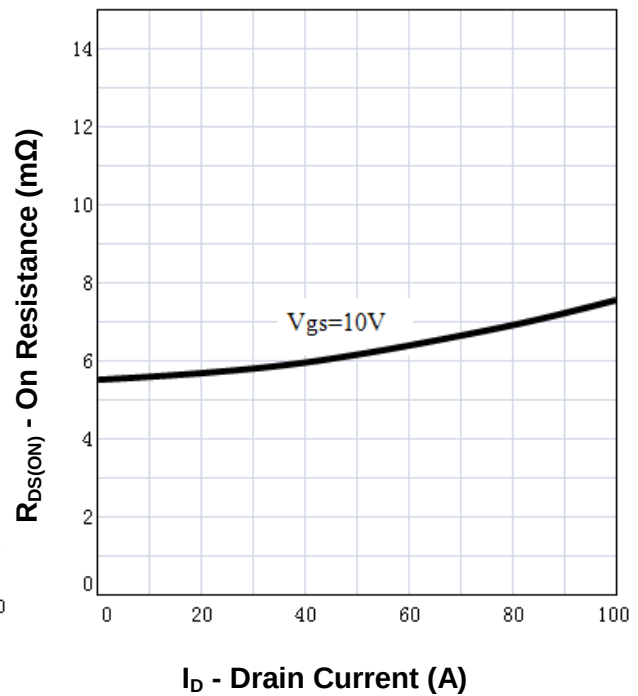


Typical Characteristics

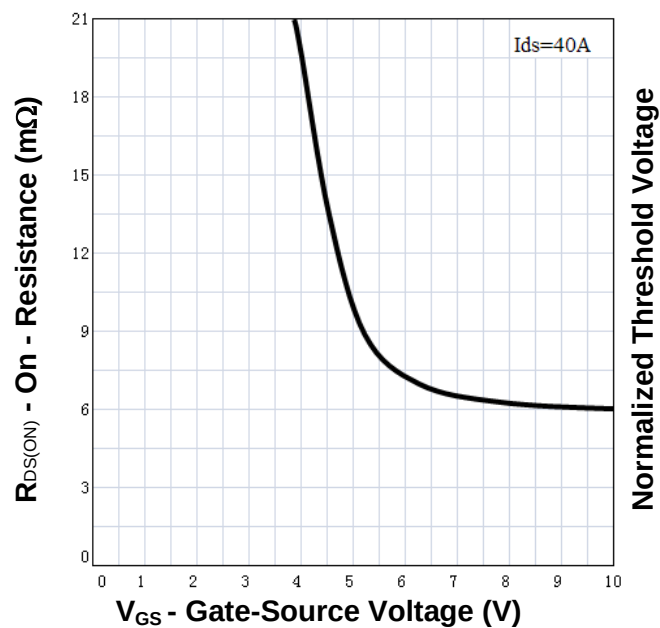
Output Characteristics



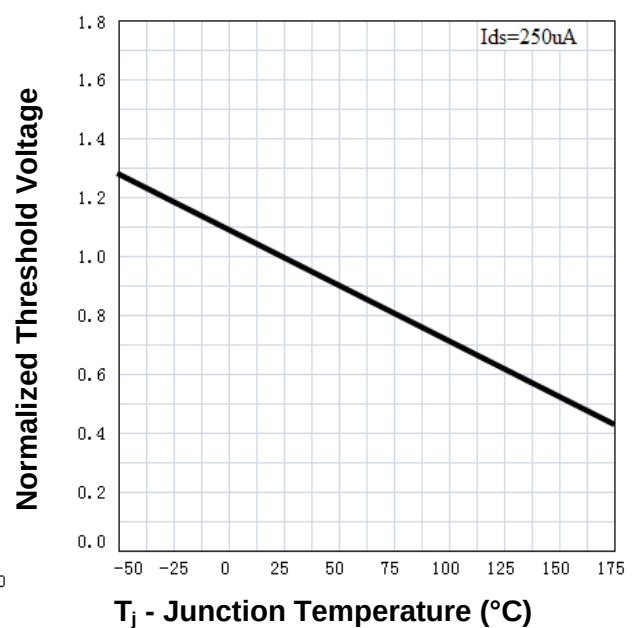
Drain-Source On Resistance



Drain-Source On Resistance

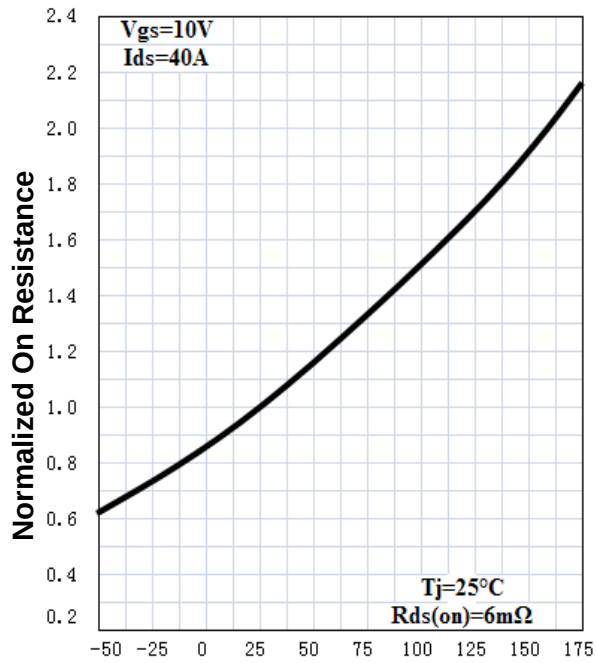


Gate Threshold Voltage



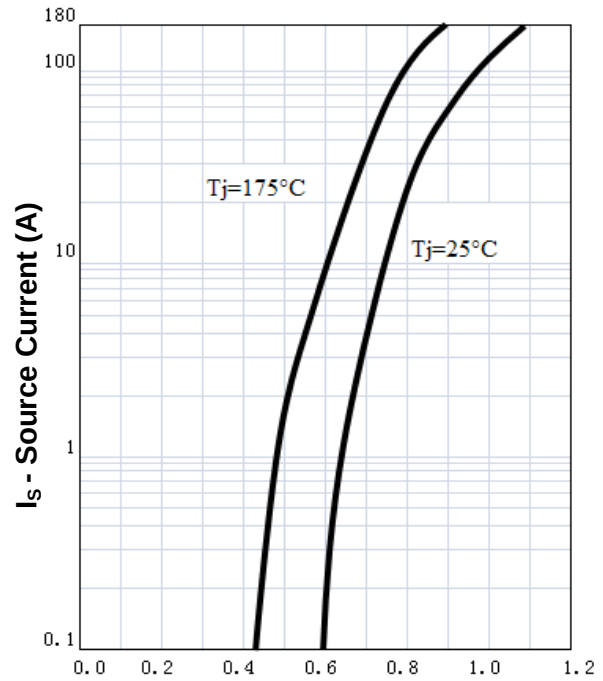
Typical Characteristics

Drain-Source On Resistance



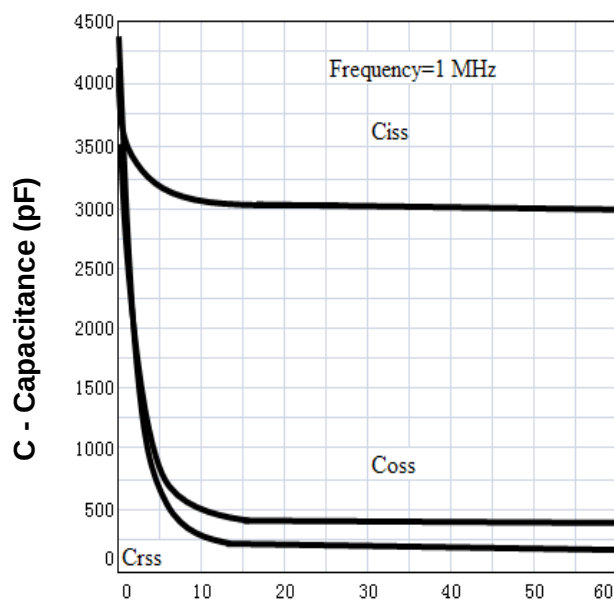
T_j - Junction Temperature (°C)

Source-Drain Diode Forward



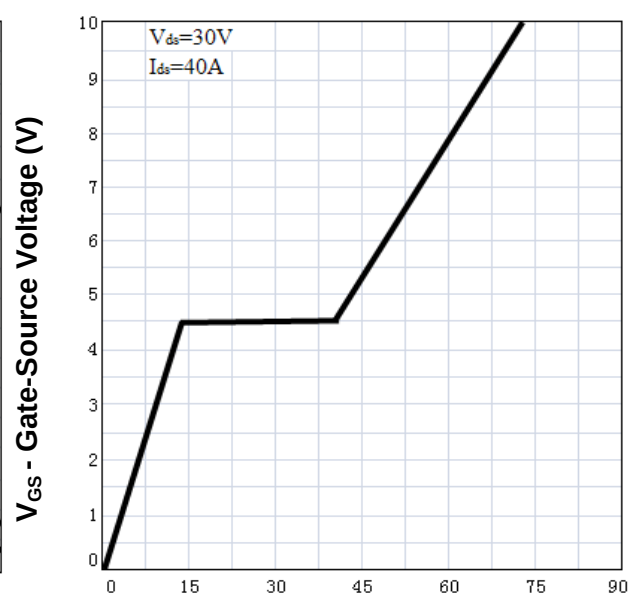
V_{SD} - Source-Drain Voltage (V)

Capacitance



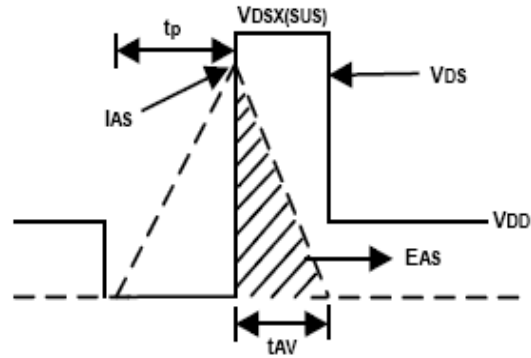
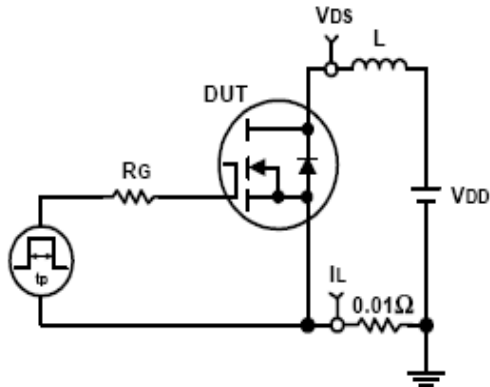
V_{DS} - Drain-Source Voltage (V)

Gate Charge

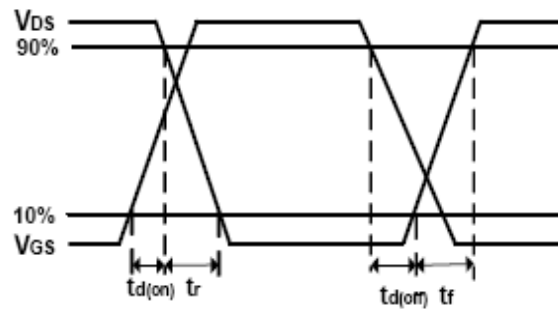
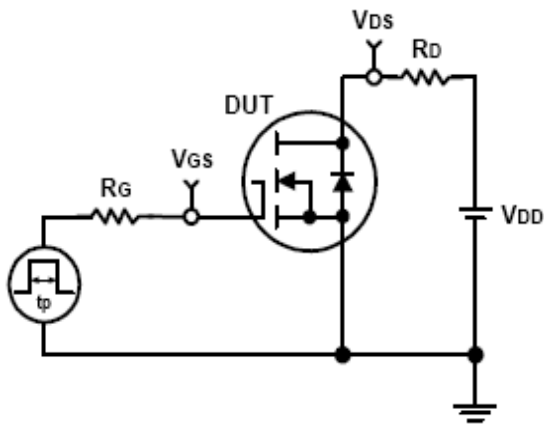


Q_G - Gate Charge (nC)

Avalanche Test Circuit and Waveforms



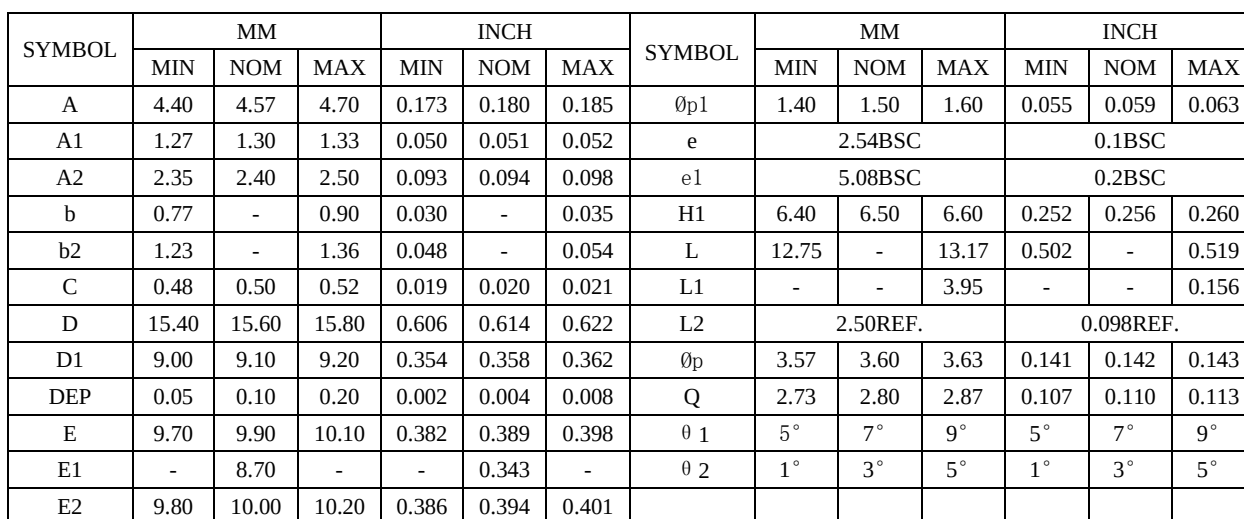
Switching Time Test Circuit and Waveforms



Ordering and Marking Information

Device	Marking	Package	Packaging	Quantity	Reel Size	Tape width
RU6099R	RU6099R	TO-220	Tube	50	-	-

TO-220FB-3L



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